

Media Alert:



Analog Bits to deliver two presentations on 16nm IP at TSMC Open Innovation Platform® Ecosystem Forum

Santa Clara, CA, September 22, 2016 – Analog Bits (www.analogbits.com), the semiconductor industry's leading provider of low-power mixed-signal IP (Intellectual Property) solutions will be presenting on two topics at the TSMC Open Innovation Platform (OIP) Ecosystem Forum. The first presentation will discuss a novel approach to keeping size small and power low, resulting in de-risking SOC development while providing increased flexibility. The second presentation, delivered jointly with Mentor Graphics, will discuss design and verifications techniques for SERDES IP development on the latest 16FFC process geometry. Analog Bits will also be demonstrating the latest low-power mixed signal IP, including their industry leading multi-standard SERDES at booth 302.

WHAT: Analog Bits latest 16nm FFC Mixed Signal IP products

SERDES IP Products

- Half-power SERDES IP supporting PCIe Gen 3/4, HMC 2.0, 10G-KR.

Clocking IP Products

- Wide range, Fine resolution and Customizable PLL & DLL IP cores

Sensors IP Products

- On-die sensors for real-time monitoring of Process, Voltage and Temperature (PVT)

WHEN: September 22, 2016

13:00 - 13:30: IP Track Presentation

- Mahesh Tirupattur, Executive Vice President, Analog Bits
- *Silicon-proven, low power IP for TSMC 16nm FFC for Automotive to Datacenter SOC's*

17:00 – 17:30 EDA Track Presentation

- Alan Rogers - President & CTO, Analog Bits
- *Design and Verification of 16nm FFC Low Power SERDES for Datacenter and Automotive Applications*

10:30 – 18:30: Ecosystem Pavilion, Booth 302

WHERE: TSMC 2016 Open Innovation Platform® Ecosystem Forum

San Jose McEnery Convention Center

150 West San Carlos St.

San Jose, CA 95113

About Analog Bits: Founded in 1995, Analog Bits, Inc. (www.analogbits.com), is the leading supplier of mixed-signal IP with a reputation for easy and reliable integration into advanced SOCs. Products include precision clocking macros such as PLLs & DLLs, programmable interconnect solutions such as multi-protocol SERDES and programmable I/O's as well as specialized memories such as high-speed SRAMs and TCAMs. With billions of IP cores fabricated in customer silicon, from 0.35-micron to 16/14-nm processes, Analog Bits has an outstanding heritage of "first-time-working" with foundries and IDMs.

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